



# 2.0A SURFACE MOUNT SCHOTTKY BARRIER RECTIFIER

PowerDI®123

#### **Features**

- Guard Ring Die Construction for Transient Protection
- High Current Capability
- Low Leakage Current
- Patented Interlocking Clip Design for High Surge Current Capacity
- Lead Free Finish, RoHS Compliant (Note 4)
- "Green" Molding Compound (No Br, Sb)
- Qualified to AEC-Q101 Standards for High Reliability

#### **Mechanical Data**

- Case: PowerDI<sup>®</sup>123
- Case Material: Molded Plastic, "Green" Molding Compound UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020D
- Terminal Connections: Cathode Band
- Terminals: Finish Matte Tin Annealed Over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Marking Information: See Page 2
- Ordering Information: See Page 2
- Weight: 0.01 grams (approximate)



Top View

### Maximum Ratings @T<sub>A</sub> = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load.

For capacitance load, derate current by 20%.

Characteristic	Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V <sub>RRM</sub> V <sub>RWM</sub> V <sub>R</sub>	40	V
RMS Reverse Voltage	V <sub>R(RMS)</sub>	28	V
Average Forward Current	I <sub>F(AV)</sub>	2.0	A
Non-Repetitive Peak Forward Surge Current 8.3ms Single Half Sine-Wave Superimposed on Rated Load	I <sub>FSM</sub>	40	A

### **Thermal Characteristics**

Characteristic	Symbol	Тур	Max	Unit
Thermal Resistance, Junction to Ambient Air (Note 1)	$R_{ heta JA}$	73	_	°C/W
Thermal Resistance, Junction to Soldering Point (Note 2)	$R_{ heta JS}$	_	13	°C/W
Operating Temperature Range	TJ	-65 t	0 +125	°C
Storage Temperature Range	T <sub>STG</sub>	-65 t	o +150	°C

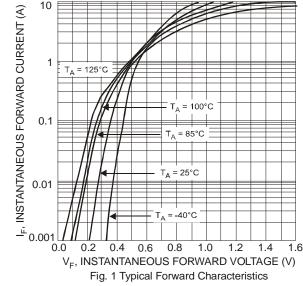
## Electrical Characteristics @T<sub>A</sub> = 25°C unless otherwise specified

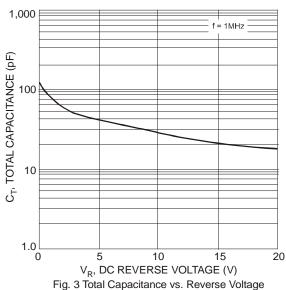
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
Reverse Breakdown Voltage (Note 3)	$V_{(BR)R}$	40	_	_	V	$I_R = 20\mu A$
Forward Voltage	V <sub>F</sub>	_	0.52 0.65	0.58 0.7	V	I <sub>F</sub> = 1.0A I <sub>F</sub> = 2.0A
Leakage Current (Note 3)	I <sub>R</sub>	_	_	20 6.0	•	V <sub>R</sub> = 40V, T <sub>A</sub> = 25°C V <sub>R</sub> = 40V, T <sub>A</sub> = 100°C
Total Capacitance	Ст	_	28	_	pF	$V_R = 10V, f = 1.0MHz$

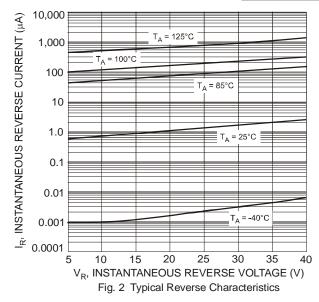
Notes:

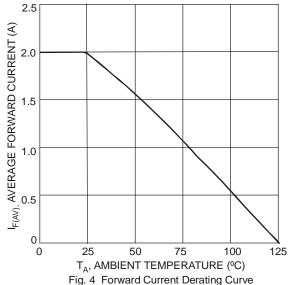
- 1. Part mounted on Polymide board with 2 oz., copper,  $74\text{mm}^2$  pad layout.  $T_A = 25^{\circ}\text{C}$
- 2. Theoretical  $R_{\text{NJS}}$  calculated from the top center of the die straight down to the PCB/cathode tab solder junction.
- 3. Short duration pulse test used to minimize self-heating effect.
- 4. EU Directive 2002/95/EC (RoHS). All applicable RoHS exemptions applied, see EU Directive 2002/95/EC Annex Notes.











### Ordering Information (Note 5)

Part Number	Case	Packaging
DFLS240-7	PowerDI®123	3000/Tape & Reel

Notes: 5. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

# **Marking Information**



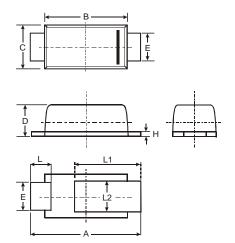
F04A = Product Type Marking Code YM = Date Code Marking Y = Year (ex: R = 2004)M = Month (ex: 9 = September)

Date Code Key

Year	2003	2004	20	005	2006	2007	2008	2009	20	010	2011	2012
Code	Р	R	;	S	T	U	V	W		X	Υ	Z
Month	Jan	Feb	Mar	Apı	r May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

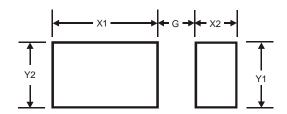


### **Package Outline Dimensions**



	PowerDI <sup>®</sup> 123						
Dim	Min	Max	Тур				
Α	3.50	3.90	3.70				
В	2.60	3.00	2.80				
С	1.63	1.93	1.78				
D	0.93	1.00	0.98				
Е	0.85	1.25	1.00				
Н	0.15	0.25	0.20				
L	0.55	0.75	0.65				
L1	1.80	2.20	2.00				
L2	0.95	1.25	1.10				
All D	All Dimensions in mm						

## **Suggested Pad Layout**



Dimensions	Value (in mm)
G	1.0
X1	2.2
X2	0.9
Y1	1.4
Y2	1.4

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